# Graphics stack

# **Contents**

1	Xorg	2
2	Mesa	2
3	Fonts	2
4	Nvidia	3
5	gfx-drm	4
	5.1 Intel KMS driver	4
	5.1.1 References	5
	5.1.2 Known Issues	5
	5.1.3 Sandy Bridge GT1	5
	5.1.4 Gen >= 8	5
	5.2 Hardware Matrix	5
	5.3 How to checkout Linux drm code only	15

Like most UNIX/Linux operating systems, OpenIndiana's GUI environment is based on the X Window System bundled with libraries and applications developed by the freedesktop.org community.

Therefore the Graphic Stack is comprised of: \* the opensource X11 display server Xorg, client libraries and utilities developed by the X.org Project, \* an OpenGL implementation in the form of the opensource Mesa library, \* and illumos kernel drivers for different graphics adapters provided by the gfx-drm gate, \* together with the libdrm implementing communication between kernel drivers and user-space components through the Direct Rendering Infrastructure (DRI) protocol.

All these components can be installed using the unified build system oi-userland or simply installed from the package repositories.

Notes concerning different components are provided:

Component	Scope
Xorg	Building the Xorg display server, protocol headers, client libraries and applications.
Mesa	Building the Mesa library and testing OpenGL capabilities.
Fonts	Packaging new fonts for OpenIndiana.
Nvidia	Installation of Nvidia's proprietary graphics driver for Solaris.
gfx-drm	Building the illumos KMS drivers and libdrm from the gfx-drm gate.

## 1 Xorg

All the Xorg components are build in oi-userland and located in the x11 directory:

https://github.com/OpenIndiana/oi-userland/tree/oi/hipster/components/x11

A bogus component is provided to rebuild the X11 gate: https://github.com/OpenIndiana/oi-userland/tree/oi/hipster/components/x11/x11-gate

The upstream Solaris code is located at:

https://github.com/oracle/solaris-userland/tree/master/components/x11

### 2 Mesa

To build Mesa use the x11/mesa component.

### 3 Fonts

Font components in oi-userland are located in the components/fonts directory.

### 4 Nvidia

Nvidia provides Solaris x86-64/x86 packages of their proprietary driver for different families of graphic adapters.

The Nvidia driver shipped with OpenIndiana is built using openindiana/nvidia-XXX e.g. openindiana/nvidia-470 component.

The list of currently supported and legacy drivers is updated on the Unix Drivers page.

Series	Label	Models	Notes
430.xx	Current long lived branch	Quadro RTX 4000-8000, Quadro, Quadro Blade, Quadro NVS, NVS	
415.xx	Current short lived branch	Quadro RTX 4000-6000, Quadro, Quadro Blade, Quadro NVS, NVS	
396.xx	Legacy GPU	GeForce 400 to Geforce GTX 10	Driver 396.24
340.xx	Legacy GPU	GeForce 8 to GeForce 700 series	Driver 340.106; Delivers libvdpau as part of the package.
304.xx	Legacy GPU	GeForce 6 to GeForce 600 series	Driver 304.137

The drivers series 340.xx, 390.xx, and 490.xx are currently part of oi-userland, others may be installed manually following Nvidia's instructions.

The 390.xx drivers are current default.

Note on switching driver versions

```
# beadm create nvidia-460
# beadm mount nvidia-460 /tmp/nvidia-460
# pkg -R /tmp/nvidia-460 uninstall xorg-video nvidia nvidia-390
# pkg -R /tmp/nvidia-460 install nvidia-460
# beadm activate nvidia-460
# init 6
```

#### Note on manual driver installations:

```
# beadm create oi-nvidia
# beadm mount oi-nvidia /mnt
# pkg -R /mnt uninstall x11/server/xorg/driver/xorg-video driver/graphics/nvidia
# /bin/sh NVIDIA-Solaris-x86-390.48.run --extract-only
# cd NVIDIA-Solaris-x86-390.48
# pkgadd -R /mnt -d . NVDAgraphics NVDAgraphicsr
# bootadm update-archive -R /mnt
# beadm unmount -f oi-nvidia
# beadm activate oi-nvidia
# init 6
```

# 5 gfx-drm

The gfx-drm gate consists of different kernel components for support of various graphic adapters in the illumos and the Direct Rendering Infrastructure (DRI) library, libdrm.

Component	Description
libdrm	The library enables communication between kernel components and user-space libraries through the DRI protocol.
agpart	Kernel driver for the Graphics Address Remapping Table (GART) / Graphics Translation Table (GTT) support.
header-drm	System headers for DRM/KMS kernel drivers and user-space libraries.
i915	Intel KMS driver for Intel Graphics Media Accelerator (GMA) and Intel HD Graphics adapters.

#### 5.1 Intel KMS driver

The xorg-video-intel package is the open-source 2D graphics driver for the X Window System as implemented by X.org. It supports a variety of Intel graphics chipsets including:

- i810/i810e/i810-dc100,i815,
- i830M,845G,852GM,855GM,865G,
- 915G/GM,945G/GM/GME,946GZ
- G/GM/GME/Q965,
- G/Q33,G/Q35,G41,G/Q43,G/GM/Q45
- PineView-M (Atom N400 series)
- PineView-D (Atom D400/D500 series)
- Intel(R) HD Graphics.
- Intel(R) Iris(TM) Graphics,
- Intel(R) Iris(TM) Pro Graphics.

Open source kernel driver(s) by Oracle:

https://github.com/oracle/solaris-userland/tree/master/components/x11/kernel/sun-src

The relevant kernel code is located in drm and i915 subfolders.

An illumos port of Oracle code is available here:

https://github.com/illumos/gfx-drm

Martin Bochnig, creator of OpenSXCE, initially backported the S12 driver to illumos available in oi-userland until December 20, 2016.

It was then superseeded by the new gate providing improvements to the gfx\_private interface and appeart driver, as well as containing userland libraries used by graphics, like libdrm.

#### 5.1.1 References

Alan Coopersmith pointed to the following documents regarding DRI/KMS:

- http://www.phoronix.com/scan.php?page=news\_item&px=Solaris-DRM-KMS-2015
- http://www.x.org/wiki/Events/XDC2015/Program/#Fishel\_status\_drm\_i915\_solaris
- https://en.wikipedia.org/wiki/Direct Rendering Manager
- http://dri.freedesktop.org/wiki/
- https://wiki.archlinux.org/index.php/kernel mode setting
- https://web.archive.org/web/20170711030533/http://lanyrd.com/topics/x-window-system/

Additionally, information about driver development:

- https://01.org/linuxgraphics
- http://dri.freedesktop.org/docs/drm/
- http://blog.ffwll.ch/2013/01/i915gem-crashcourse-overview.html
- blog posts from the i915 Linux maintainer http://blog.ffwll.ch/

#### 5.1.2 Known Issues

**5.1.2.1 SNA** SNA may cause segmentation faults if enabled. If so, just disable SNA and you can copy the attached 20-intel-uxa.conf to /etc/X11/xorg.conf.d/.

For now UXA mode is set by default.

### 5.1.3 Sandy Bridge GT1

A hardware bug required implementing a workaround in the Intel ringbuffer implementation, occasional 1-2 second hangs may occur.

#### 5.1.4 Gen >= 8

Generation 8 (and later) devices are not supported at all. They require Intel ringbuffer support, which is not implemented.

#### **5.2 Hardware Matrix**

#### Reference:

- https://github.com/torvalds/linux/blob/master/include/drm/i915 pciids.h
- · https://en.wikipedia.org/wiki/List of Intel graphics processing units
- http://src.illumos.org/source/xref/gfx-drm/usr/src/uts/intel/io/i915/i915 drv.c#294

Generation amily		Codename	PCI Model id	i915 support	xf86-video-intel support
2nd	1830	Almador	0x3577l830M, l830MG		
2nd	1845G	Brookdale	0x2562845G, I845GL, I845GV		

Generatio <del>ra</del> mily		Codename	PCI Model id	i915 support	xf86-video-intel support
2nd 185X		Montara	0x3582855GM		
2nd	185X	Montara	0x358d855GM		
2nd	1865G	Springdale	0x2572865G		
3rd	1915G	Grantsdale	0x2582915G		
3rd	1915G	Grantsdale	0x258æ7221G		
3rd	1915GM	Alviso	0x2592915GM		
3rd	1945G	Lakeport	0x2772945G		
3rd	1945GM	Calistoga	0x27a2945GM		
3rd	1945GM	Calistoga	0x27ad945GME		
3rd	G33	Bearlake	0x29b2Q35G		
3rd	G33	Bearlake	0x29c2G33G		
3rd	G33	Bearlake	0x29d2Q33G		
3rd	<b>PINEVIEW</b>	Pineview	0xa001		
3rd	PINEVIEW	Pineview	0xa011		
4th	1965G	Lakeport	0x2972946GZ		
4th	1965G	Bearlake	0x2982G35G		
4th	1965G	Broadwater	0x2992965Q		
4th	1965G	Broadwater	0x29a2965G		
4th	1965GM	Crestline	0x2a02965GM		
4th	1965GM	Crestline	0x2a12965GME		
4th	GM45	Cantiga	0x2a42GM45G		
4th	G45	Eaglelake	0x2e02GDEG		
4th	G45	Eaglelake	0x2e12Q45G		
4th	G45	Eaglelake	0x2e22G45G		
4th	G45	Eaglelake	0x2e32G41G		
4th	G45	Eaglelake	0x2e42B43G		
4th	G45	Eaglelake	0x2e92B43G.1		
5th	IRONLAKE	•	0x0042		
	D	(Clarkdale)			
5th	IRONLAKE	` ,	0x0046		
	М	(Arrandale)			
6th	SANDY BRIDGE	Sandy Bridge	0x0102		
6th	D SANDY BRIDGE D	Sandy Bridge	0x0112		
6th	SANDY BRIDGE	Sandy Bridge	0x0122		
6th	D SANDY BRIDGE D	Sandy Bridge	0x010A		

Genera	atio <del>r</del> amily	Codename	PCI Model id	i915 xf86-video-intel support support
6th	SANDY BRIDGE M	Sandy Bridge	0x0106	HW bug causes GPU hangs. Work- around in place, but one may see brief render
6th	SANDY BRIDGE M	Sandy Bridge	0x0116	delays. HW bug causes GPU hangs. Work- around in place, but one may see brief render
6th	SANDY BRIDGE M	Sandy Bridge	0x0126	delays. HW bug causes GPU hangs. Work- around in place, but one may see brief render
7th	IVY BRIDGE M	Ivy Bridge	0x0156GT1 mobile	delays.
7th	IVY BRIDGE M	Ivy Bridge	0x016GT2 mobile	
7th	IVY BRIDGE D	Ivy Bridge	0x0152GT1 desktop	

Generatio <del>r</del> amily		Codename	PCI Model id	i915 support	xf86-video-intel support
7th	IVY BRIDGE	Ivy Bridge	0x0162GT2 desktop		
7th	D IVY BRIDGE D	Ivy Bridge	0x015aGT1 server		
7th		Ivy Bridge	0x016aGT2 server		
7th	HASWELL D	Haswell	0x0402GT1 desktop		
7th	HASWELL D	Haswell	0x0412GT2 desktop		
7th	HASWELL D	Haswell	0x0422GT3 desktop		
7th	HASWELL D	Haswell	0x040aGT1 server		
7th	HASWELL D	Haswell	0x041aGT2 server		
7th	HASWELL D	Haswell	0x042aGT3 server		
7th	HASWELL D	Haswell	0x040BGT1 reserved		
7th	HASWELL D	Haswell	0x041BGT2 reserved		
7th	HASWELL D	Haswell	0x042EGT3 reserved		
7th	HASWELL D	Haswell	0x040EGT1 reserved		
7th	HASWELL D	Haswell	0x041EGT2 reserved		
7th	HASWELL D	Haswell	0x042EGT3 reserved		
7th	HASWELL D	Haswell	0x0C02SDV GT1 desktop		
7th	HASWELL D	Haswell	0x0C1 <b>2</b> SDV GT2 desktop		
7th	HASWELL D	Haswell	0x0C2SDV GT3 desktop		
7th	HASWELL D	Haswell	0x0C0 <b>&amp;</b> DV GT1 server		
7th	HASWELL D	Haswell	0x0C1&DV GT2 server		
7th	HASWELL D	Haswell	0x0C2 <b>&amp;</b> DV GT3 server		
7th	HASWELL D	Haswell	0x0C0 <b>B</b> DV GT1 reserved		

Generationamily  7th HASWELL		Codename	PCI Model id	i915 support	xf86-video-intel support
		Haswell	0x0C1 <b>B</b> DV GT2		
	D	"	reserved		
7th	HASWELL	Haswell	0x0C2BDV GT3		
7.1.	D		reserved		
7th	HASWELL	Haswell	0x0C0BDV GT1		
7+b	D D	Heavell	reserved		
7th	HASWELL	Hasweii	0x0C1BDV GT2		
7th	D HASWELL	Hacwoll	reserved 0x0C2BDV GT3		
<i>t</i> u i	D	пазмен	reserved		
7th	HASWELL	НасмаП	0x0A02JLT GT1		
7 (11	D	Haswell	desktop		
7th	HASWELL	Haswell	0x0A1 <b>2</b> JLT GT2		
	D	ridovicii	desktop		
7th	HASWELL	Haswell	0x0A2 <b>2</b> JLT GT3		
	D	. idorron	desktop		
7th	HASWELL	Haswell	0x0A0AJLT GT1		
	D		server		
7th	HASWELL	Haswell	0x0A1AJLT GT2		
	D		server		
7th	HASWELL	Haswell	0x0A2AJLT GT3		
	D		server		
7th	HASWELL	Haswell	0x0A0BJLT GT1		
	D		reserved		
7th	HASWELL	Haswell	0x0A1BJLT GT2		
	D		reserved		
7th	HASWELL	Haswell	0x0A2BJLT GT3		
	D		reserved		
7th	HASWELL	Haswell	0x0D02CRW GT1		
	D		desktop		
7th	HASWELL	Haswell	0x0D12CRW GT2		
<b>-</b>	D	"	desktop		
7th	HASWELL	Haswell	0x0D22CRW GT3		
7+b	D D	Heavell	desktop		
7th	HASWELL	Haswell	0x0D0ÆRW GT1		
7+h	D HACMELL	Hacwoll	Server		
7th	HASWELL D	пазиен	0x0D1&RW GT2		
7th	HASWELL	Haswell	server 0x0D2 <b>&amp;</b> RW GT3		
<i>i</i> u i	D D	HUSWEII	server		
7th	HASWELL	Haswell	0x0D0 <b>E</b> CRW GT1		
	D	IIGOVVCII	reserved		
7th	HASWELL	Haswell	0x0D1ECRW GT2		
	D		reserved		
7th	HASWELL	Haswell	0x0D2ECRW GT3		
	D		reserved		

Generation Tamily  7th HASWELL D		Codename	PCI Model id	i915 support	xf86-video-intel support
		Haswell	0x0D0ÆRW GT1 reserved		
7th	HASWELL D	Haswell	0x0D1ERW GT2 reserved		
7th	HASWELL D	Haswell	0x0D2ERW GT3 reserved		
7th	HASWELL M	Haswell	0x0406GT1 mobile		
7th	HASWELL M	Haswell	0x0416GT2 mobile		
7th	HASWELL M	Haswell	0x0426GT2 mobile		
7th	HASWELL M	Haswell	0x0C065DV GT1 mobile		
7th	HASWELL M	Haswell	0x0C165DV GT2 mobile		
7th	HASWELL M	Haswell	0x0C265DV GT3 mobile		
7th	HASWELL M	Haswell	0x0A06JLT GT1 mobile		
7th	HASWELL M	Haswell	0x0A16ULT GT2 mobile		
7th	HASWELL M	Haswell	0x0A26JLT GT3 mobile		
7th	HASWELL M	Haswell	0x0A0ÆJLX GT1 mobile		
7th	HASWELL M	Haswell	0x0A1EJLX GT2 mobile		
7th	HASWELL M	Haswell	0x0A2EJLT GT3 reserved		
7th	HASWELL M	Haswell	0x0D0 <b>©</b> RW GT1 mobile		
7th	HASWELL M	Haswell	0x0D16CRW GT2 mobile		
7th	HASWELL M	Haswell	0x0D26CRW GT3 mobile		
7th	VALLEY VIEW M	Valley View (Bay Trail)	0x0f30		
7th	VALLEY VIEW M	Valley View (Bay Trail)	0x0f31		
7th	VALLEY VIEW M	Valley View (Bay Trail)	0x0f32		
7th	VALLEY VIEW M	Valley View (Bay Trail)	0x0f33		
7th	VALLEY VIEW M	Valley View (Bay Trail)	0x0157		

Generatio <del>F</del> amily		Codename	PCI id	Model	i915 support	xf86-video-intel support
7th	VALLEY VIEW D	Valley View (Bay Trail)	0x01	55		
8th	BROADWI GT12M	EL <b>B</b> roadwell	0x160	02GT1 ULT	Gen >=8 <b>NOT</b> sup- ported at all.	
8th	BROADWI GT12M	EL <b>B</b> roadwell	0x160	06GT1 ULT		
8th	BROADWI GT12M	ELBroadwell	0x160	DBGT1 Iris		
8th	BROADWI GT12M	∃LBroadwell	0x160	EGT1 ULX		
8th	BROADWI GT12M	ELBroadwell	0x161	L2GT2 Halo		
8th	BROADWI GT12M	ELBroadwell	0x161	L6GT2 ULT		
8th	BROADWI GT12M	ELB⊾roadwell	0x162	LBGT2 ULT		
8th	BROADWI GT12M	ELBroadwell	0x162	LEGT2 ULX		
8th	BROADWI GT12D	ELBroadwell	0x160	OAGT1 Server		
8th	BROADWI GT12D	ELBroadwell	0x160	DGT1 Workstation		
8th	BROADWI GT12D	<b>ELB</b> roadwell	0x161	LAGT2 Server		
8th	BROADWI GT12D	<b>ELB</b> roadwell	0x161	LDGT2 Workstation		
8th	BROADWI GT3M	<b>ELB</b> roadwell	0x162	22GT3 ULT		
8th	BROADWI GT3M	EL <b>B</b> roadwell	0x162	26GT3 ULT		
8th	BROADWI GT3M	ELB⊾roadwell	0x162	2BGT3 Iris		
8th	BROADWI GT3M	∃B⊾roadwell	0x162	2EGT3 ULX		
8th		∃Æroadwell	0x162	2AGT3 Server		
8th		ELB⊾roadwell	0x162	2DGT3 Workstation		
8th		ELBroadwell	0x163	B2JLT		
8th		ELΒαroadwell	0x163	36ULT		

Generatio <del>F</del> amily		nily Codename		Model	i915 support	xf86-video-intel support
RSVDM		ELBroadwell	0x163	<b>B</b> ris		
8th BROADWEL <b>B</b> road RSVDM		ELBroadwell	0x163	EULX		
8th	BROADWE RSVDD	ELBroadwell	0x163	<i>A</i> Server		
8th	BROADWE RSVDD	ELBroadwell	0x163	<b>D</b> Workstation		
8th	CHERRY VIEW	Cherry View (Braswell, Cherry Trail)	0x22b	0		
8th	CHERRY VIEW	Cherry View (Braswell, Cherry Trail)	0x22b	1		
8th	CHERRY VIEW	Cherry View (Braswell, Cherry Trail)	0x22b	2		
8th	CHERRY VIEW	Cherry View (Braswell, Cherry Trail)	0x22b	3		
9th	SKYLAKE GT1	Skylake	0x190	ØLT GT1		
9th	SKYLAKE GT1	Skylake	0x190	EULX GT1		
9th	SKYLAKE GT1	Skylake	0x190	2DT GT1		
9th	SKYLAKE GT1	Skylake	0x190	BHalo GT1		
9th	SKYLAKE GT1	Skylake	0x190	ASRV GT1		
9th	SKYLAKE GT2	Skylake	0x191	&ULT GT2		
9th	SKYLAKE GT2	Skylake	0x192	1ULT GT2F		
9th	SKYLAKE GT2	Skylake	0x191	EJLX GT2		
9th	SKYLAKE GT2	Skylake	0x191	2DT GT2		
9th	SKYLAKE GT2	Skylake	0x191	BHalo GT2		
9th	SKYLAKE GT2	Skylake	0x191	ASRV GT2		
9th	SKYLAKE GT2	Skylake	0x191	DWKS GT2		
9th	SKYLAKE GT3	Skylake	0x192	3JLT GT3		

Generatio <del>r</del> amily		Codename	PCI Model id	i915 support	xf86-video-intel support
9th	SKYLAKE GT3	Skylake	0x1926JLT GT3		
9th	SKYLAKE GT3	Skylake	0x1927ULT GT3		
9th	SKYLAKE GT3	Skylake	0x192BHalo GT3		
9th	SKYLAKE GT3	Skylake	0x192ASRV GT3		
9th	SKYLAKE GT4	Skylake	0x1932DT GT4		
9th	SKYLAKE GT4	Skylake	0x193BHalo GT4		
9th	SKYLAKE GT4	Skylake	0x193DWKS GT4		
9th	SKYLAKE GT4	Skylake	0x193 <i>A</i> SRV GT4		
9th	BROXTON	(Apollo Lake)	0x0A84		
9th	BROXTON	(Apollo Lake)	0x1A84		
9th	BROXTON	(Apollo Lake)	0x1A85		
9th	BROXTON	(Apollo Lake)	0x5A84APL HD Graphics 505		
9th	BROXTON	(Apollo Lake)	0x5A85APL HD Graphics 500		
9th	KABY LAKE GT1	Kaby Lake	0x5913JLT GT1.5		
9th	KABY LAKE GT1	Kaby Lake	0x5915JLX GT1.5		
9th	KABY LAKE GT1	Kaby Lake	0x5917DT GT1.5		
9th	KABY LAKE GT1	Kaby Lake	0x590&JLT GT1		
9th	KABY LAKE GT1	Kaby Lake	0x590EJLX GT1		
9th	KABY LAKE GT1	Kaby Lake	0x5902DT GT1		
9th	KABY LAKE GT1	Kaby Lake	0x5908Halo GT1		

Generatio <del>F</del> amily		Codename	PCI Model id	i915 support	xf86-video-intel support
9th	KABY LAKE	Kaby Lake 0x590BHalo GT1			
9th	GT1 KABY LAKE GT1	Kaby Lake	0x590ASRV GT1		
9th	KABY LAKE GT2	Kaby Lake	0x5916ULT GT2		
9th	KABY LAKE GT2	Kaby Lake	0x5921ULT GT2F		
9th	KABY LAKE GT2	Kaby Lake	0x591EJLX GT2		
9th	KABY LAKE GT2	Kaby Lake	0x5912DT GT2		
9th	KABY LAKE GT2	Kaby Lake	0x591 <b>B</b> Halo GT2		
9th	KABY LAKE GT2	Kaby Lake	0x591 <i>A</i> SRV GT2		
9th	KABY LAKE GT2	Kaby Lake	0x591DWKS GT2		
9th	KABY LAKE GT3	Kaby Lake	0x5923JLT GT3		
9th	KABY LAKE GT3	Kaby Lake	0x5926JLT GT3		
9th	KABY LAKE GT3	Kaby Lake	0x5927ULT GT3		
9th	KABY LAKE GT4	Kaby Lake	0x593BHalo GT4		
9th	COFFEE LAKE	Coffee Lake	0x3E9@T1		
9th	COFFEE LAKE	Coffee Lake	0x3E9DT GT2		
9th 9th	COFFEE LAKE COFFEE	Coffee Lake Coffee Lake	0x3E92DT GT2 0x3E93GT1		
<b>-</b>	LAKE	Jones Lane	3,020011		

Generation Family		Codename	PCI Model id	i915 support	xf86-video-intel support
9th	COFFEE LAKE	Coffee Lake	0x3E94GT2		
9th	COFFEE LAKE	Coffee Lake	0x3E9 <b>5</b> GT3		
9th	COFFEE LAKE	Coffee Lake	0x3E9 <b>6</b> CT2		
9th	COFFEE LAKE	Coffee Lake	0x3E9 <b>K</b> 5T2		
9th	COFFEE LAKE	Coffee Lake	0x3EA <b>6</b> 5T3		
9th	COFFEE LAKE	Coffee Lake	0x3EATGT3		
9th	COFFEE LAKE	Coffee Lake	0x3EA <b>&amp;</b> T3		
9th	CANNON LAKE	Cannon Lake	0x5A5 <b>2</b> GT2		
9th	CANNON LAKE	Cannon Lake	0x5A5AGT2		
9th	CANNON LAKE	Cannon Lake	0x5A4 <b>2</b> GT2		
9th	CANNON LAKE	Cannon Lake	0x5A4 <b>A</b> GT2		
9th	CANNON LAKE	Cannon Lake	0x5A51GT2		
9th	CANNON LAKE	Cannon Lake	0x5A59GT2		
9th	CANNON LAKE	Cannon Lake	0x5A4 <b>G</b> T2		
9th	CANNON LAKE	Cannon Lake	0x5A4 <b>9</b> GT2		
9th	CANNON LAKE	Cannon Lake	0x5A7 <b>1</b> GT2		
9th	CANNON LAKE	Cannon Lake	0x5A79GT2		

# 5.3 How to checkout Linux drm code only

```
$ mkdir linux-drm
```

<sup>\$</sup> cd linux-drm/

<sup>\$</sup> git init

<sup>\$</sup> git remote add origin https://github.com/freedesktop/drm-intel.git

<sup>\$</sup> git config core.sparsecheckout true

<sup>\$</sup> echo "drivers/gpu/drm/\*" >> .git/info/sparse-checkout

<sup>\$</sup> echo "include/drm/\*" >> .git/info/sparse-checkout

<sup>\$</sup> git pull --depth=1 origin drm-intel-fixes